



# Indian Institute of Information Technology Sri City, Chittoor

(An Institute of National importance under an Act of Parliament)

Advt. No: IIITS/Acad/Ph.D./2022/05/6069

9th July 2022

## PhD Admissions (Part-Time) - Monsoon 2022

Indian Institute of Information Technology Sri City Chittoor (IIITS) was established by Government of India as an Institute of National Importance under the Act of Parliament along with Government of Andhra Pradesh and Industry Partners-Sri City Foundation and Sri City Pvt. Limited.

IIITS is located in Sri City ([www.sricity.in](http://www.sricity.in)), a decade-old state of the art industrial city located about 60 KMs North of Chennai on “Chennai – Nellore – Kolkata highway” (AH45). Sri City is spread over 8000 acres encompassing a multi-product Special Economic Zone (SEZ), Domestic Tariff Zone (DTZ), Free Trade & Warehousing Zone (FTWZ) and Electronics Manufacturing Cluster. Sri City is hosting over 120 companies from 27 countries. The institute has access to the industries and social infrastructure available in Sri City through the Industry Partner.

IIITS envisions to be a globally known institution for IT education, research and development. The institute has special thrust to attract and retain talented faculty members who can make a mark in teaching and research at the international level. The current faculty members at IIITS are from leading universities from India and abroad with excellent teaching and research credentials.

Applications are invited from motivated middle and senior-level Professionals of IT industries and Scientists in Govt. of India Research Labs for admissions in Ph.D. (Part-Time) Programme [Monsoon 2022].

### 1. PhD (Part-Time)

(Only for working IT professionals employment based in India and scientists from Govt. of India Research Labs such as DRDO, ISRO etc.)

Research Areas (CSE):

Agent based modeling & simulations, Algorithms, Authentication and Access Control, Cognitive modelling - relational patterns, Computational Geometry, Computer Architecture, Computer networks, Cryptography and Network Security, Cyber Physical Systems, Data Analytics, Design Automation of Electronic Systems, Distributed Algorithms, EEG Data Analysis, Embedded Systems, System architecture, Healthcare informatics, High performance computing, Human Computer Interaction, Image Processing and Computer Vision, Intelligent Control, Machine/Deep Learning, , Multi-objective optimization, Natural Language Processing, Security and Blockchain, Self Organizing and Self-Assembly Systems, Soft Computing, Spatial/Spatio Temporal/Multivariate Statistical modeling, Statistical and machine learning models for Environmental Applications, Text Data Mining /

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Information Retrieval, VR/AR, Web/Information/Cloud/IoT/SDN Security, Wireless Sensor Systems, Human Robot Interaction, Unmanned Aerial Vehicles, Advanced Cryptography, AI driven intrusion Detection System, Bioinformatics and Computational Biology with Deep Learning, Bio-Medical Image/Data Analysis using Artificial Intelligence.

## Research Areas (ECE):

Adaptive Driver Assistance System, Applications of Pattern Recognition, Biomedical Signal Processing, Cyber Physical Systems, Deep learning, Electromagnetic scattering, Energy harvesting, MEMS and VLSI Technology, Microfabrication, Microfluidics and micropumps, Passive components, Propagation modeling, Protocols for IoT, Sensor Technology, Speaker recognition, Speech/voice activity detection, Statistical Signal Processing, Vehicular Communication, Wireless Networks, Quadrotor Control, Renewable Energy.

## Eligibility:

Working IT Professionals with rich technical background from IT industry/MNCs with employment and residence based in India or Scientists working in Govt. of India Research labs such as DRDO, ISRO etc. Additionally, the candidates should also fulfil any one of the following criteria:

- a) Master's degree (M.E./M.Tech) with bachelor's (B.E./B.Tech) degree in the applicable areas in engineering/technology with a minimum of 60% (or above) aggregate marks (CGPA  $\geq$  6.5/10) in UG and PG for admission under GEN/GEN-EWS/OBC-NCL category and 55% (or above) aggregate marks (CGPA of  $\geq$  6.0) for SC/ST/PwD candidates, currently working in technical areas or CSE / ECE or related areas.
- b) B.E./B.Tech. degree in the applicable areas (in engineering/technology in any branch) with a minimum of 60% (or above) aggregate marks (CGPA  $\geq$  6.5/10) in UG for admission under GEN/GEN-EWS/OBC-NCL category and 55% (or above) aggregate marks (CGPA of  $\geq$  6.0) for SC/ST/PwD candidates and a minimum of three years of documented experience in technical areas CSE / ECE or related areas.

## **PhD (Part-Time) Admission Process:**

1. Candidates recommended by the Ph.D. Admissions Committee will be called to appear for a written test\*.

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2. The candidates shortlisted based on the written test performance need to appear for a personal interview before the Ph.D. Admissions Interview panel.

\* Syllabus for the written test is provided in Appendix I.

## 2. Application Fee:

Application Fee of Rs. 500/- for GEN/GEN-EWS/OBC-NCL category and Rs. 200/- for SC/ST/PwD candidates (to be paid through SB Collect only) and the the transaction receipt must be uploaded in google form and attach a hard copy with the application. The details on the payment through SB collect are given below.

## 3. Last date for all Applications:

Candidates may submit the detailed application using Google Form:

<https://forms.gle/plqgfudzkQT9skD7A>

Note: NoC and other format to be submitted by the applicant are also available on the website:

Last date for submission of applications via online with relevant documents is **1st August 2022**.

Late date for submission of hard copy of the application with related documents via speed post/courier is **6th August 2022**.

For any further queries, you may write an email to [phd.admissions@iiits.in](mailto:phd.admissions@iiits.in)

## 4. Fee Payment Through SB Collect:

- Payment through State Bank Collect portal only
- Applicants have to visit **www.onlinesbi.com** and follow the process
- Click on State Bank Collect, and accept terms and conditions
- Select - Andhra Pradesh
- Select - Educational institutions
- Select - Indian Institute of Information Technology Sri City Chittoor
- Select - PhD Application Fee
- Enter Name and other details. Click Submit
- Choose your Payment option
- Pay Application Fee
- Please download receipts generated in SB collect for record
- Please upload SB collect payment receipt while filling up the application form (Google Form link is given above) for the payment confirmation

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## 5. Additional Information:

The Fee structure for the Part-Time is given below. Admission fee and caution deposit has to be paid only when joining the PhD program. Further, applicable fees must be paid at the beginning of each semester.

### Fee Structure:

Fee Category	Amount
Admission Fee	Rs. 15000.00 (non-refundable) - one time payment
Caution Deposit	Rs. 15000.00 (refundable after successful completion of the program)
Tuition Fee	Rs. 60,000.00 per Semester

Note: These fees are subject to revisions from time to time.

### Contact us:

<u>For Admission related queries:</u>		
Mrs. Suneetha	73064 73364	phd.admissions@iiits.in
Mrs Neha Sharma	83748 49552	admissions.support@iiits.in
<u>Programme related queries:</u>		
Dr. Raja Vara Prasad	73373 24920	asstdean.pgr@iiits.in
Dr. Sreeja SR	78935 94848	phd.coordinator@iiits.in

**Disclaimer:** The Institute reserves the right to accept/reject any or all applications without assigning any reason and also the institute reserves the right to modify/cancel the application/admission process at any point in time without assigning any reason.

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## Appendix I

### Syllabus for Written Test :

#### Computer Science and Engineering:

**Discrete Mathematics:** Propositional and First-order Logic. Sets, Relations, Functions, Partial Orders, and Lattices, Groups, Graphs: Connectivity, Matching, Coloring Combinatorics: Counting, Recurrence Relations, Generating Functions.

**Probability:** Random Variables. Uniform, Normal, Exponential, Poisson, and Binomial Distributions. Mean, Median, Mode, and Standard Deviation. Conditional Probability and Bayes Theorem.

**Computer Programming:** Programming in C, Scope of Variables, Loops, Functions, Structures, Pointers, Dynamic Memory Allocation, File Management.

**Data Structures:** Stacks, Queues, Linked Lists, Trees, Binary Search Trees, Binary Heaps, Graphs.

**Algorithms:** Searching, Sorting, Hashing. Asymptotic Worst-case Time and Space Complexity, Algorithm Design Techniques: Greedy, Dynamic Programming, and Divide-and-Conquer. Graph Search, Minimum Spanning Trees, Shortest Paths.

**Operating System:** Processes, Threads, Interprocess Communication, Concurrency, and Synchronization, Deadlock, CPU Scheduling, Memory Management, and Virtual Memory, File Systems.

**Computer Networks:** Concept of Layering, LAN Technologies (Ethernet), Switching, TCP/UDP and Sockets, Congestion Control, Application Layer, Network Security: Authentication, Basics of Public Key and Private Key Cryptography, Digital Signatures and Certificates



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## Electronics and Communication Engineering:

### Basic electronics

Circuit laws, theorems (superposition, thevenin and norton, max power), passive elements, transients (RL and RC circuits) and op amps and applications, filters active and passive). Diode characteristics, half wave and full wave rectifier. Transistors, and amplifiers.

### Electromagnetics

Maxwell's equation, boundary conditions, time-varying fields, wave propagation in different media, transmission line basics, terminated transmission line and the special cases, antenna characteristics, FRIIS transmission equation and radar range equation.

### Digital logic design

Combinational logic: Logic circuits of Code Converters such as Binary to Gray Code Converter, BCD converter etc., K-maps, multiplexers, decoders, PROMs and PLAs  
Sequential circuits: Latches and flip-flops, counters and shift-registers  
Data converters: sample and hold circuits, ADCs and DACs; Memories.

### Signal Processing

Classification of signals and systems, basic deterministic signals, Linear systems and convolution, frequency domain representation of continuous and discrete signals, sampling theory, z-transform, circular convolution, discrete Fourier transform, Fast Fourier Transform, Digital filter design and implementation.

### Analog and Digital Communication

Basics of Analog and Digital modulation, Basics of Cellular, Interference, Noise, Channel, Wireless channel fading, Shannon Capacity.

### Embedded Systems

Basic Definitions, Embedded - C concepts, Python Programming, Real-Time Operating systems, microcontroller and its submodules. Additional topics: Knowledge on hardware boards like Arduino and raspberry Pi, sensors and actuators.

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